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PATENT
Attorney Docket No.: DB000575-023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Keeth, et al.)
Serial No.: 09/934,795)
Filed: 22 August 2001)
Entitled: 256 MEG DYNAMIC RANDOM ACCESS MEMORY)

Examiner: Not yet assigned
Art Unit: 2816

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SECOND PRELIMINARY AMENDMENT

Preliminary to the examination of the above-identified application, please amend that application as follows.

In the claims

Please amend claims 174 and 175 as follows.

174. (Amended) A method of controlling the conduction of a pair of isolation transistors in a sense amplifier responsive to an array, comprising [the steps of]:
rendering the pair of transistors conductive with a control signal that is a boosted version of the voltage used by the array; and
rendering the pair of transistors nonconductive by removing said control signal.

175. (Amended) The method of claim 174 wherein said step of rendering the pair of transistors conductive includes the step of rendering the transistors conductive with a control signal that is approximately a $[V_t]$ V_{th} higher than the voltage used by the array.